IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of : David Tester

Serial No. : 09/981,474

Filed : October 17, 2001

For : Prescaler Architecture Capable of Non Integer Division

Group Art Unit : 2115

Examiner : Suryawanshi, Suresh K.

Atty Docket : 1496.00157 / 01-587

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the date below:

Manu Kashyap

12/22/05
Date

Signature

SUBMISSION OF FORMAL DRAWINGS PURSUANT TO 37 C.F.R. §1.85

Official Draftsman

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicant hereby substitutes the enclosed formal drawings for those presently in the above referenced application.

LSI Logic Corporation
1621 Barber Lane, MS D-106
Milipitas, CA 95035
408-433-7475

Date: 12/22/05

Respectfully submitted,

Sandeep Jaggi

Reg. No. 43,331